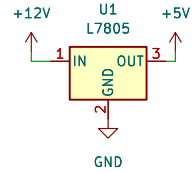
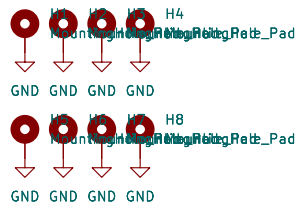
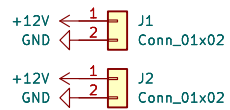


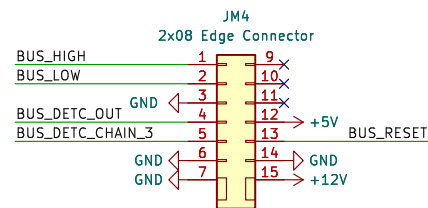
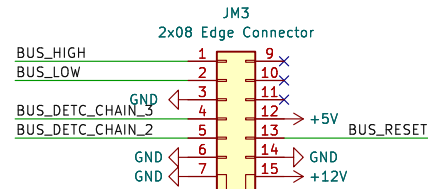
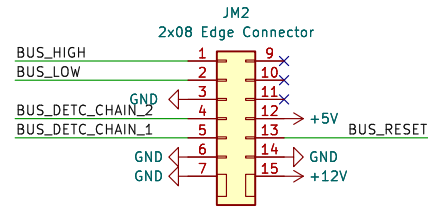
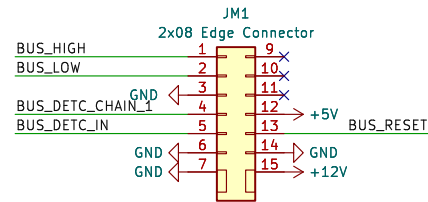
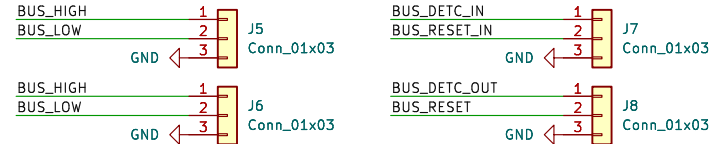
Voltage Regulator



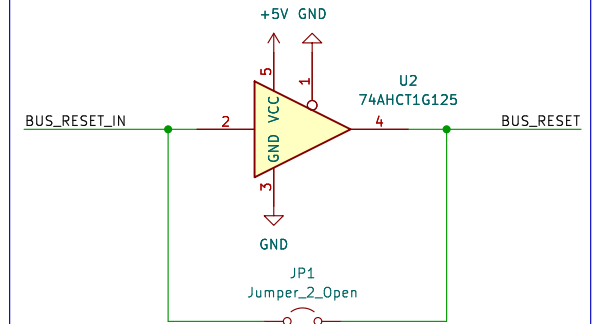
Power Connector



Bus Connectors



Reset Line Buffer



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Sheet: /
File: splitflap-backplane.kicad_sch

Title: Split-Flap Module Backplane

Size: A4 Date: 2025-10-11

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Rev: 0

Id: 1/1